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# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

756—10-3-90
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OCT 17 1990

RECEIVED OCT -4 1990 CORRESPONDENCE SECTION

Applicants:      K. KATSURA, et al.

Serial No.:      07/302,332

Filed:           January 27, 1989

For:             GRAPHIC PROCESSING APPARATUS UTILIZING IMPROVED
                 DATA TRANSFER TO REDUCE MEMORY SIZE

Art Unit:        231

Examiner:        Herndon, H.

RECEIVED OCT 14 1990 CORRESPONDENCE SECTION

## AMENDMENT UNDER 37 CFR 1.312(a)

Honorable Commissioner of Patents                    September 10, 1990
and Trademarks
Washington, D.C.  20231

Sir:

## IN THE CLAIMS:

Please amend the claims as follows:

Claim 8,  line 9, delete "lcoations" and insert -- locations
--.

Claim 9,  line 19, insert -- control -- after "memory".

Ok To Enter HEH 11-8-90

Do NOT ENTER HEH 11-8-90

10. (amended) A graphic processing apparatus comprising:

memory means, including a plurality of memory locations
in an array of columns, having corresponding column addresses,
and rows, having corresponding row addresses, for storing data;

data processing means for specifying a row address of
memory locations in said memory means for transfer of a data word
therewith;

memory control means;